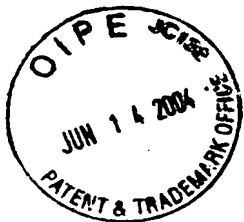


**AMENDMENT TO THE DRAWINGS:**

The four attached drawing sheets replace the pending three drawing sheets. The four replacement sheets include changing FIGURE 1 to FIGURE 1A, changing FIGURE 2 to FIGURE 2A and an additional figure, FIGURE 1B. FIGURE 1B does not add new matter but further illustrates the invention as suggested by the Examiner.

Attachment: Replacement Sheets 1-4



System and Method for Determining Capacitance for Large-Scale Integrated Circuits

KAPUR 5-10

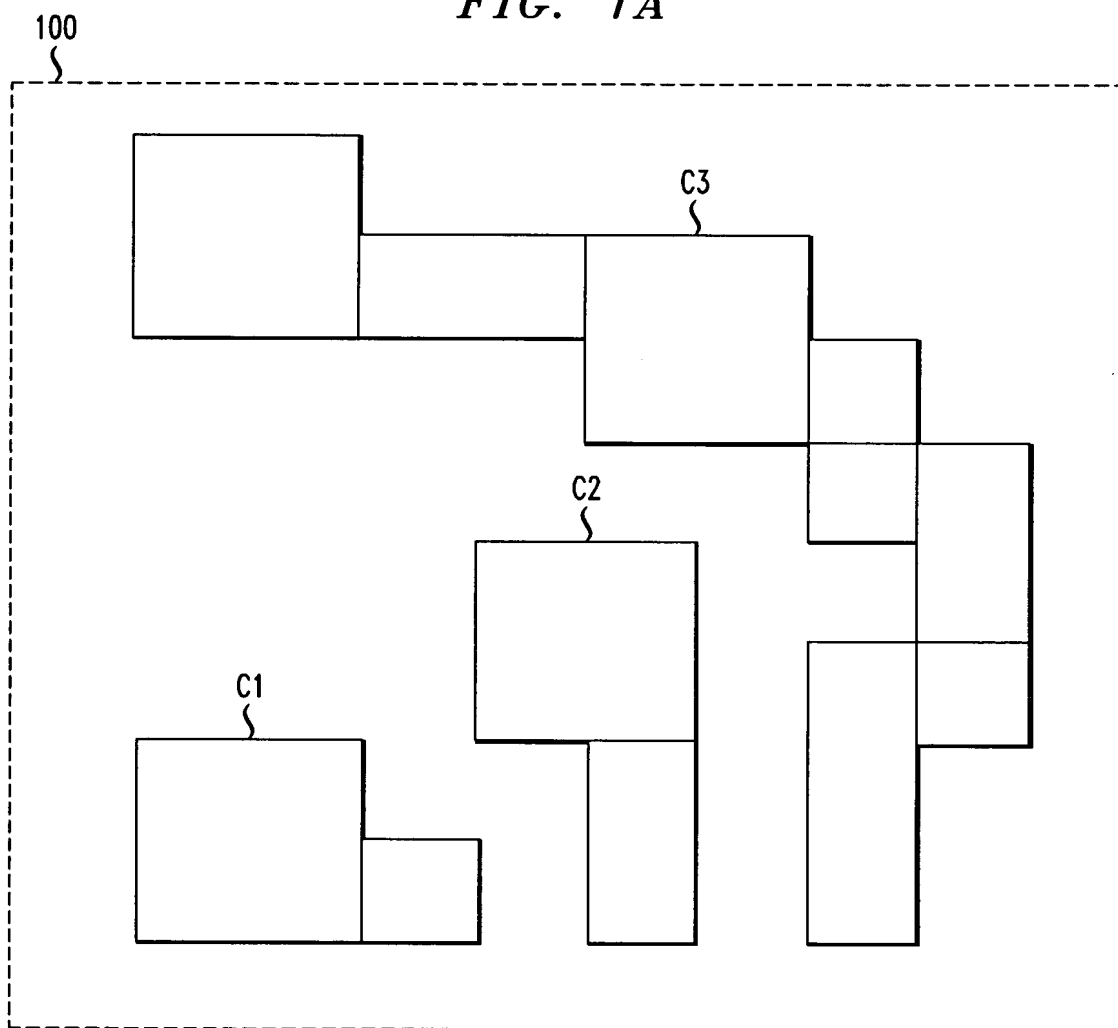
Serial No.: 09/427,238

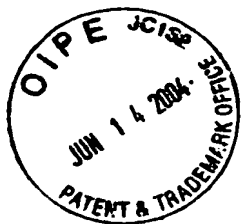
Hitt Gaines, PC; D. H. Hitt (972) 480-8800

REPLACEMENT SHEET

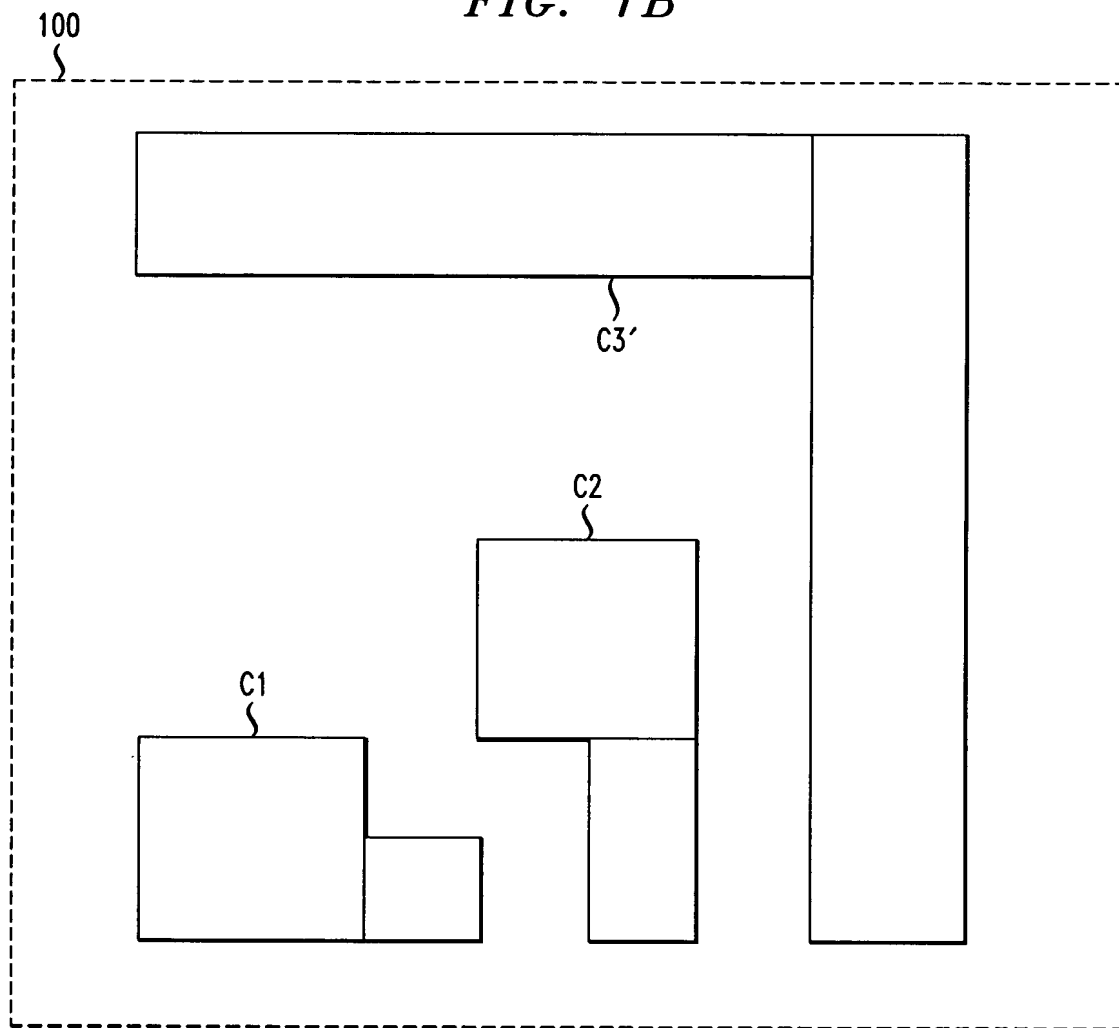
1/4

*FIG. 1A*

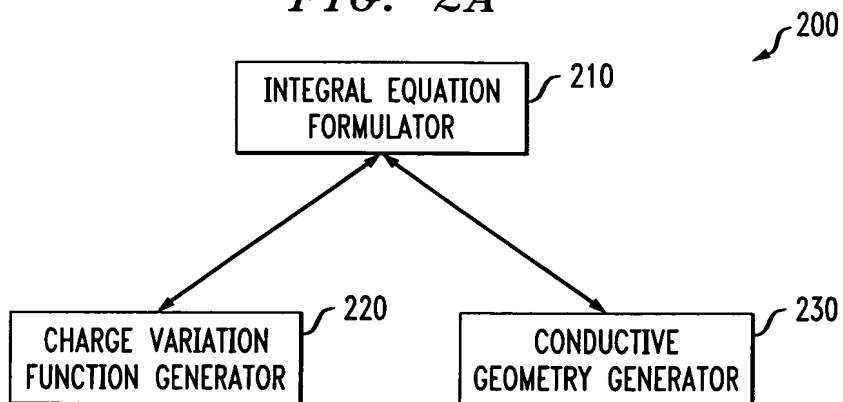




*FIG. 1B*



*FIG. 2A*



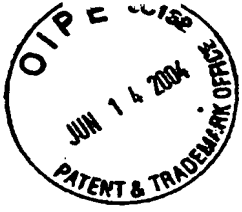
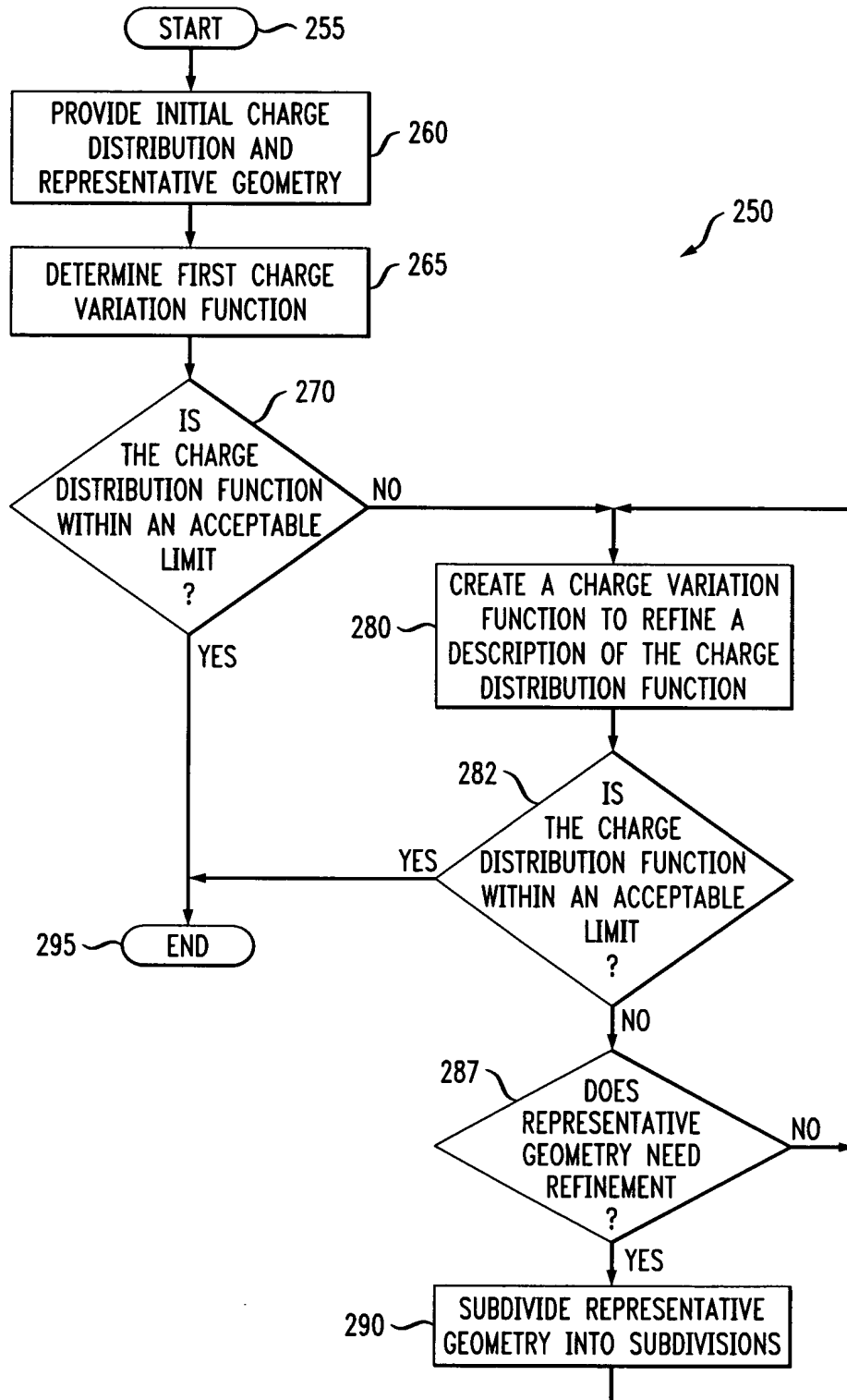
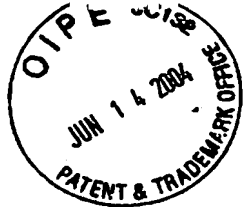


FIG. 2B





# System and Method for Determining Capacitance for Large-Scale Integrated Circuits

KAPUR 5-10

Serial No.: 09/427,238

Hitt Gaines, PC; D. H. Hitt (972) 480-8800

REPLACEMENT SHEET

4/4

FIG. 3A

300

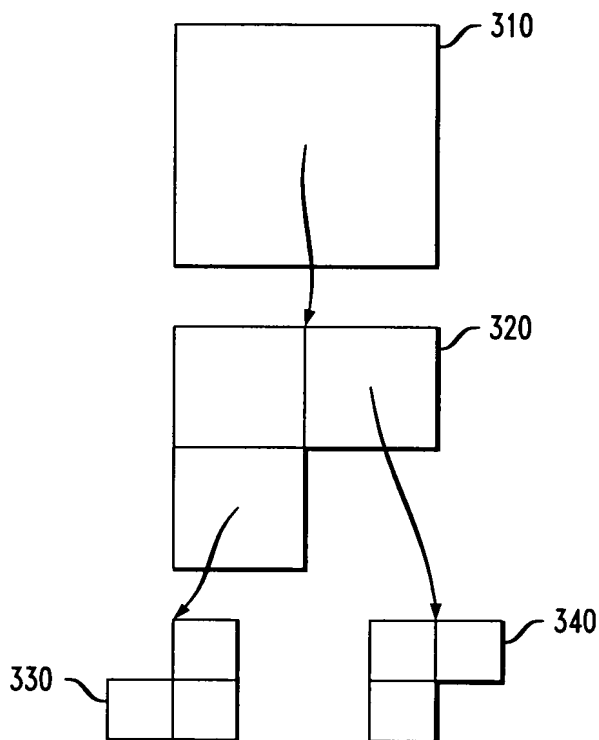


FIG. 3B

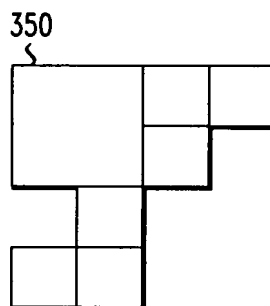


FIG. 4

CUBES	FDM TIME	GEOMETRY TIME	TOTAL TIME	MEMORY
3,100	1.3	0.4	2.4	30
7,700	3.5	0.9	5.9	42
13,500	6.8	1.6	11.3	59
33,500	17.7	3.6	29	130
78,000	44	7.2	64	280
167,000	100	15	142	610